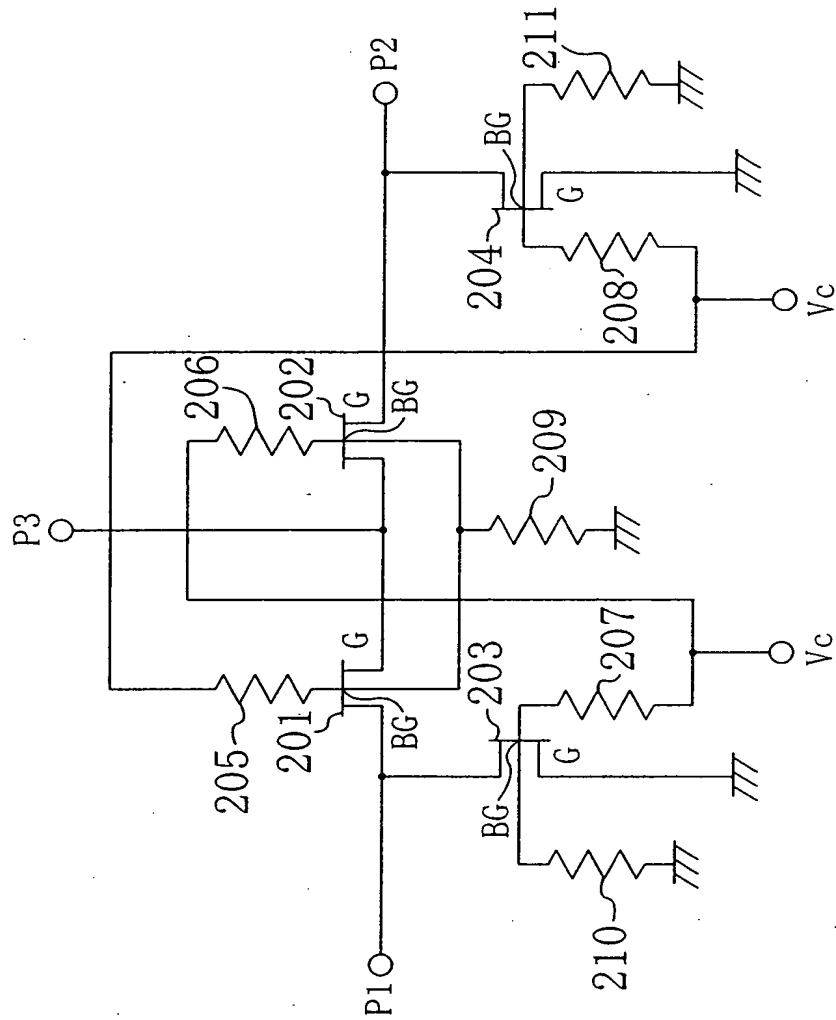


Fig. 12 is a cross-sectional view of a semiconductor device. It shows a substrate 120 with a layer 121. A central region 124 contains a stack of layers 127, 128, and 129. Vertical structures 129 are formed on the sides of the central region. Arrows labeled "II b" indicate the direction of the view.

[illegible]



FIG. 8



PRIOR ART